 {mem[3],mem[2],mem[1],mem[0]}=32'b000000000000\_00000\_010\_00001\_0000011 ; //lw x1, 0(x0)  //0

                                             {mem[7],mem[6],mem[5],mem[4]}=32'b000000000100\_00000\_010\_00010\_0000011 ; //lw x2, 4(x0)//4

                                             {mem[11],mem[10],mem[9],mem[8]}= 32'b000000001000\_00000\_010\_00011\_0000011 ; //lw x3, 8(x0)  //8

                                             {mem[15],mem[14],mem[13],mem[12]}=32'b0000000\_00000\_00000\_000\_00000\_0110011 ; //add x0, x0, x0 //0

                                             {mem[19],mem[18],mem[17],mem[16]}=32'b0000000\_00010\_00001\_110\_00100\_0110011 ; //or x4, x1, x2   //25

                                             {mem[23],mem[22],mem[21],mem[20]}=32'b0000000\_00011\_00000\_010\_01100\_0100011; //sw x3, 12(x0)  //12

                                             //{mem[27],mem[26],mem[25],mem[24]}=32'b0100000\_00011\_00100\_000\_01110\_0110011 ; //sub x10, x4, x3  //0

                                            {mem[27],mem[26],mem[25],mem[24]}=32'b0\_000001\_00011\_00100\_000\_0000\_0\_1100011; //beq x4, x3, 16 //0

                                             {mem[31],mem[30],mem[29],mem[28]}=32'b0000000\_00010\_00001\_000\_00011\_0110011 ; //add x3, x1, x2 //26

                                             {mem[35],mem[34],mem[33],mem[32]}=32'b0000000\_00010\_00011\_000\_00101\_0110011 ; //add x5, x3, x2  //35

                                             {mem[39],mem[38],mem[37],mem[36]}=32'b0100000\_00011\_00100\_000\_01010\_0110011 ; //sub x10, x4, x3  //-1

                                             {mem[43],mem[42],mem[41],mem[40]}=32'b000000001100\_00000\_010\_00110\_0000011 ; //lw x6, 12(x0)  //12

                                             {mem[47],mem[46],mem[45],mem[44]}=32'b0000000\_00000\_00000\_000\_00000\_0110011 ; //add x0, x0, x0   //0

                                             {mem[51],mem[50],mem[49],mem[48]}=32'b0000000\_00001\_00110\_111\_00111\_0110011 ; //and x7, x6, x1 //17

                                             {mem[55],mem[54],mem[53],mem[52]}=32'b0000000\_00110\_00000\_000\_00100\_0110011 ; //add x8, x6, x0  //25

                                             {mem[59],mem[58],mem[57],mem[56]}=32'b0100000\_00011\_00100\_000\_01010\_0110011 ; //sub x10, x4, x3  //0

                                             {mem[63],mem[62],mem[61],mem[60]}=32'b0000000\_00000\_00000\_000\_00000\_0110011 ; //add x0, x0, x0  //0

                                             {mem[67],mem[66],mem[65],mem[64]}=32'b000000001100\_00000\_010\_00110\_0000011 ; //lw x6, 12(x0)  //12

                                             {mem[71],mem[70],mem[69],mem[68]}=32'b00000000000000000111\_00111\_0110111; //lui x5,5 //

                                             {mem[75],mem[74],mem[73],mem[72]}=32'b00000000000000000111\_00111\_0010111; //AUIPC x5,5 //

                                             {mem[79],mem[78],mem[77],mem[76]}=32'b0\_0000010000\_0\_00000000\_00001\_1101111 ; //JAL  x1, 16 //72, 84

                                             {mem[83],mem[82],mem[81],mem[80]}=32'b000000001000\_00110\_000\_00010\_1100111; // JALR x2, x6, 8 //datainp=72, pc=20

                                             {mem[87],mem[86],mem[85],mem[84]}=32'b000000001000\_00000\_000\_00001\_0010011 ; //addi x1,x0,12  //8

                                              //{mem[75],mem[74],mem[73],mem[72]}=32'b0000000\_00001\_00110\_111\_00111\_0110011 ; //and x7, x6, x1 //17

//                                             {mem[75],mem[74],mem[73],mem[72]}=32'b0000000\_00001\_00110\_111\_00111\_0110011 ; //and x7, x6, x1 //17

//                                             {mem[79],mem[78],mem[77],mem[76]}=32'b0000000\_00110\_00000\_000\_00100\_0110011 ; //add x8, x6, x0  //25

//                                             {mem[83],mem[82],mem[81],mem[80]}=32'b0100000\_00010\_00001\_000\_01000\_0110011 ; //sub x8, x1, x2  //8

//                                             {mem[87],mem[86],mem[85],mem[84]}=32'b0000000\_00000\_00000\_000\_00000\_0110011 ; //add x0, x0, x0  //0

//                                             {mem[91],mem[90],mem[89],mem[88]}=32'b0000000\_00010\_00001\_000\_00000\_0110011 ; //add x0, x1, x2  //0

//                                             {mem[95],mem[94],mem[93],mem[92]}=32'b0000000\_00000\_00000\_000\_00000\_0110011 ; //add x0, x0, x0  //0

//                                             {mem[99],mem[98],mem[97],mem[96]}=32'b0000000\_00001\_00000\_000\_01001\_0110011 ; //add x9, x0, x1 //17

                                             //fill ur instreuctions from 100

                                              {mem[203],mem[202],mem[201],mem[200]}=32'b00000000000000000000000000010001; //17

                                              {mem[207],mem[206],mem[205],mem[204]}=32'b00000000000000000000000000001001; //9

                                              {mem[211],mem[210],mem[209],mem[208]}=32'b00000000000000000000000000011001; //25